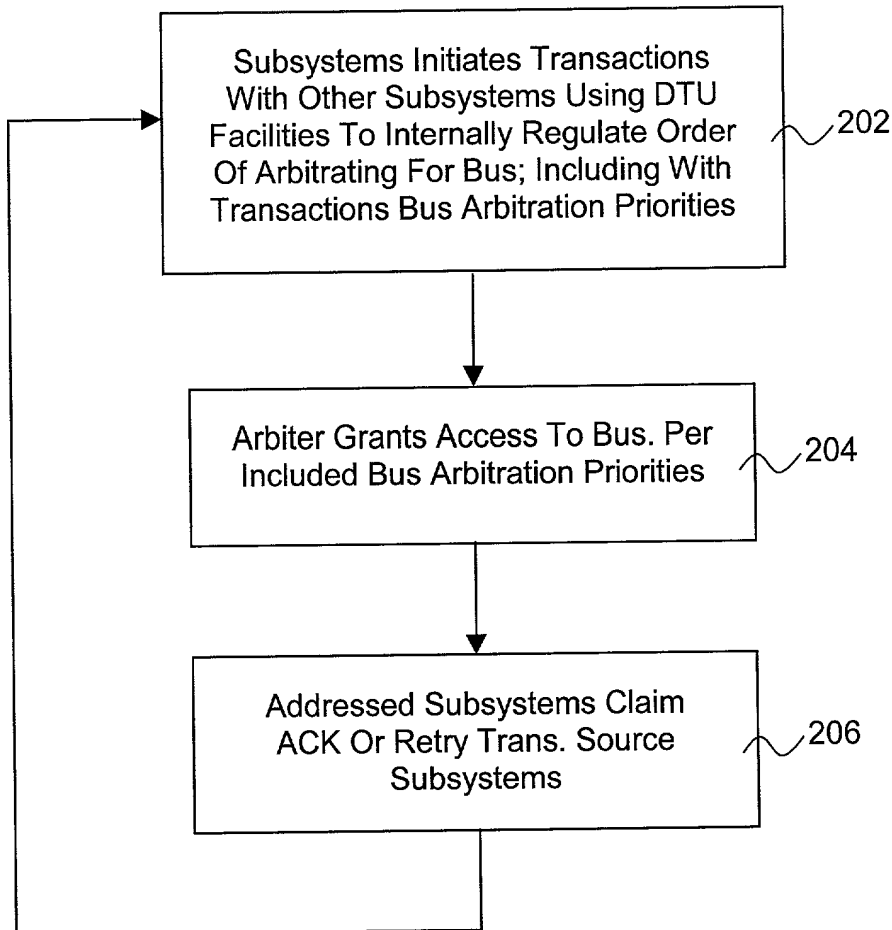


Figure 1

**Figure 2**

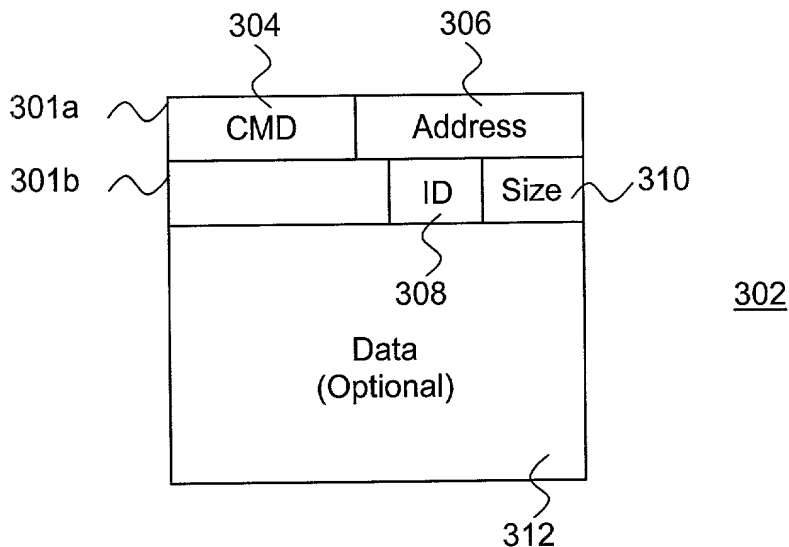


Figure 3a

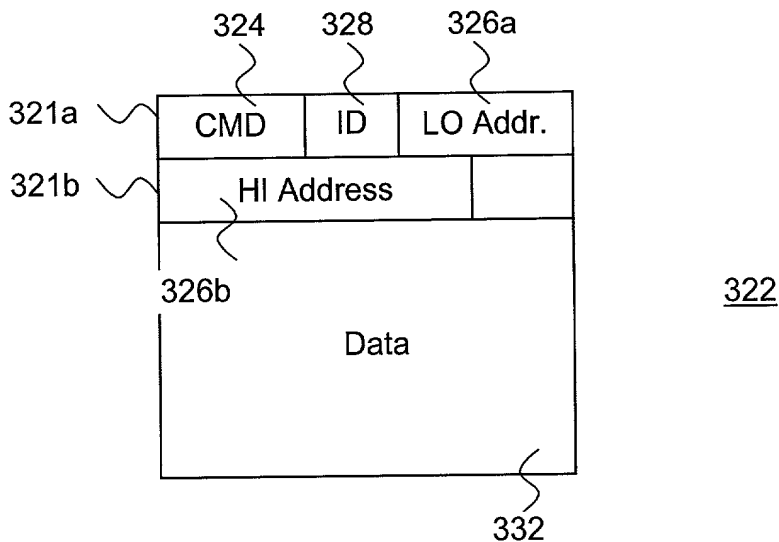


Figure 3b

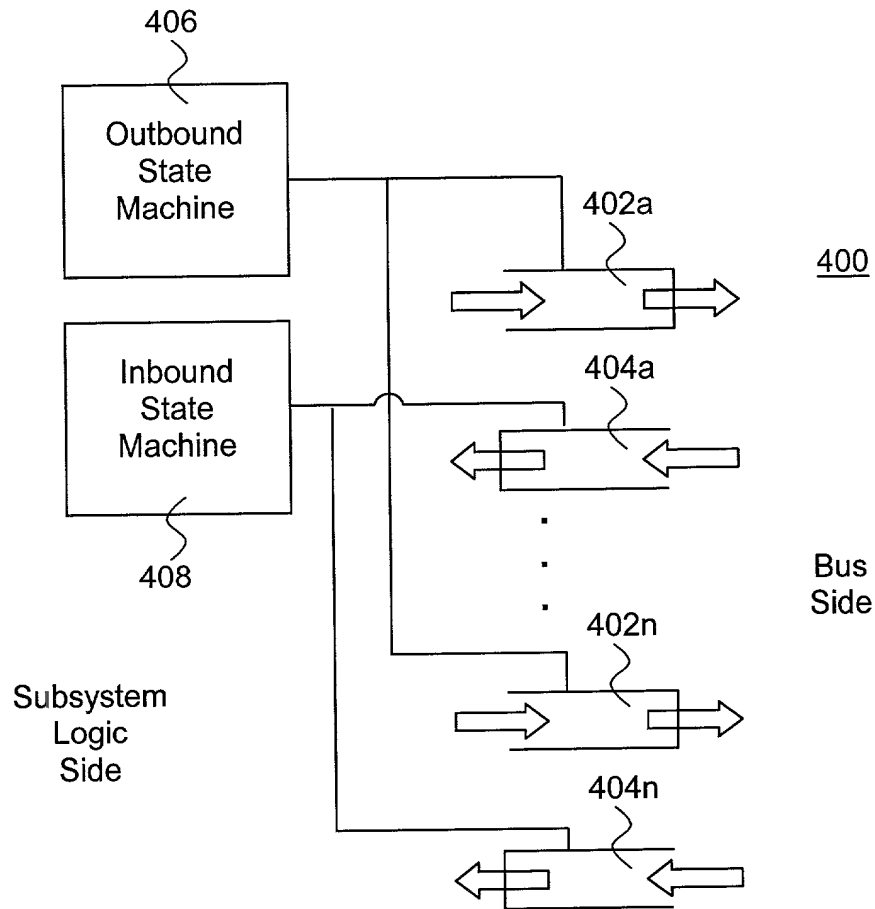


Figure 4

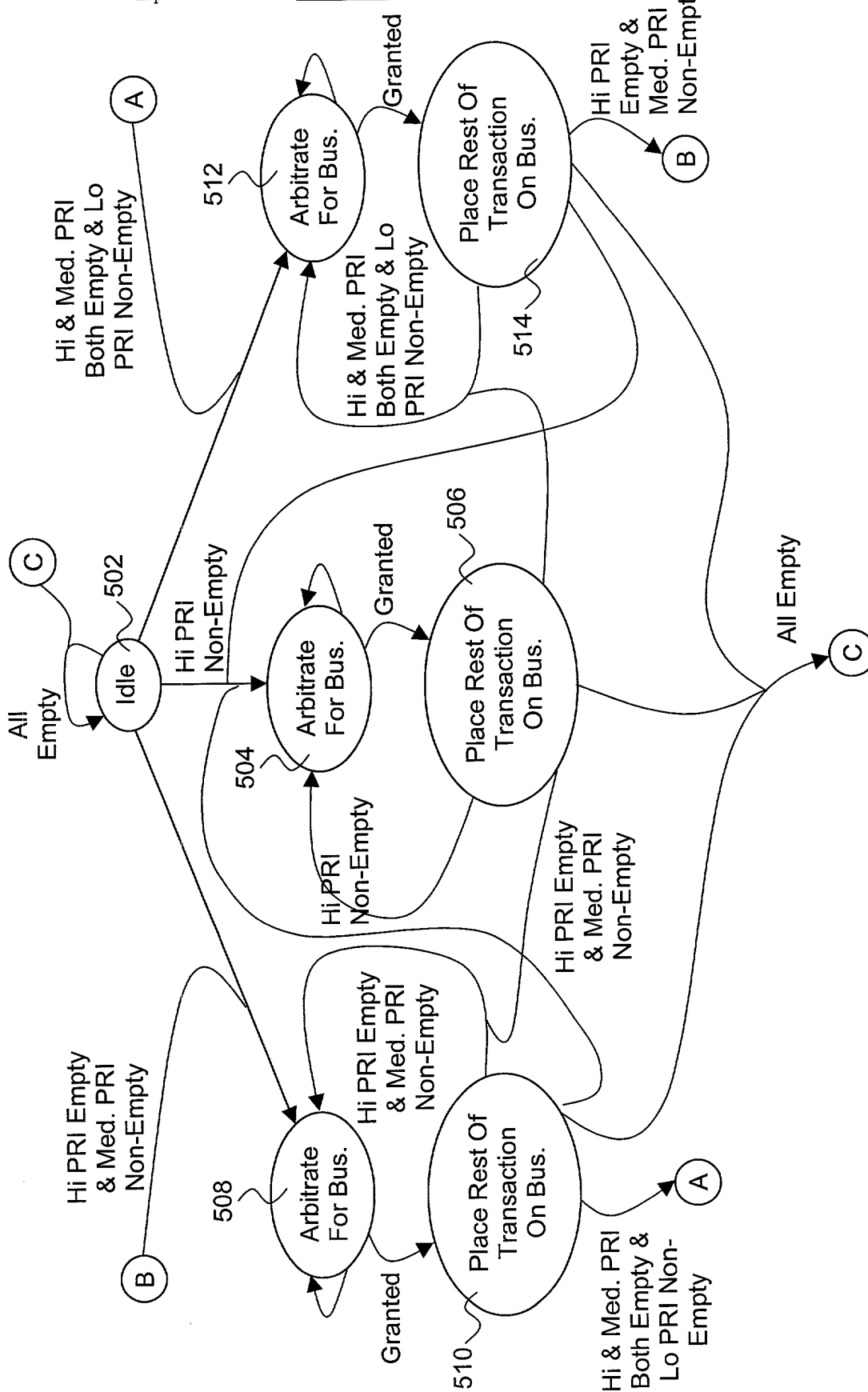


Figure 5a

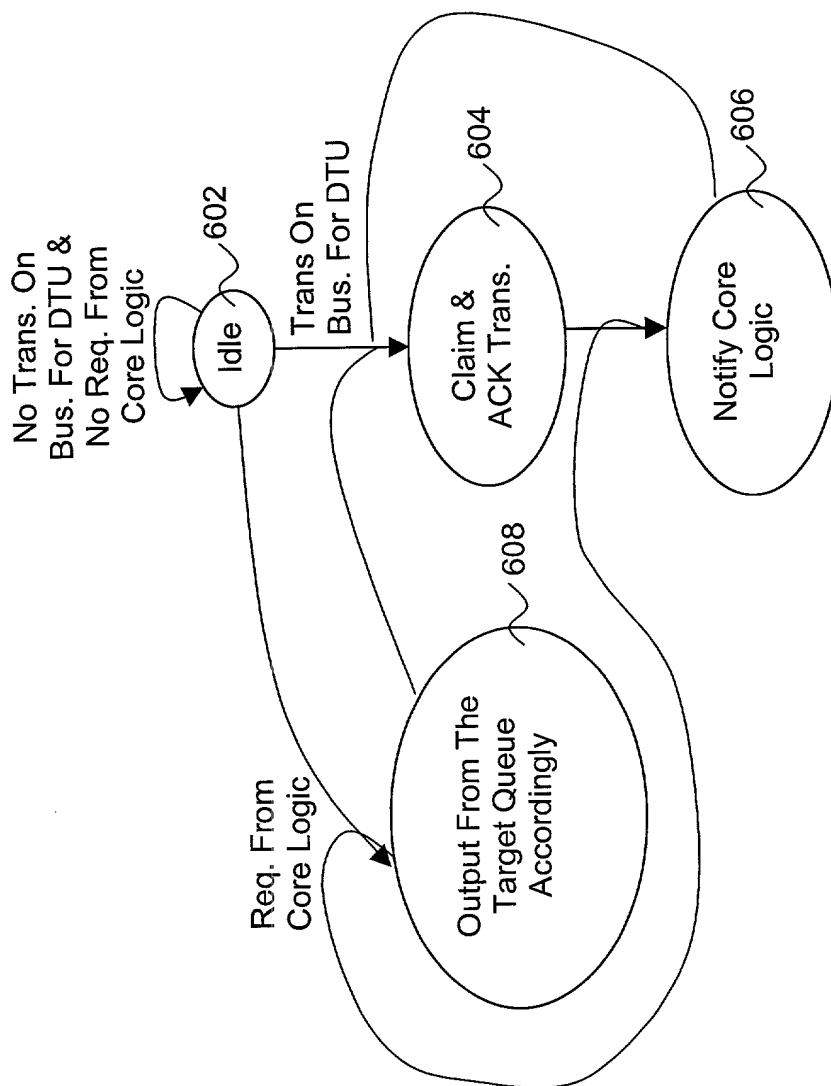
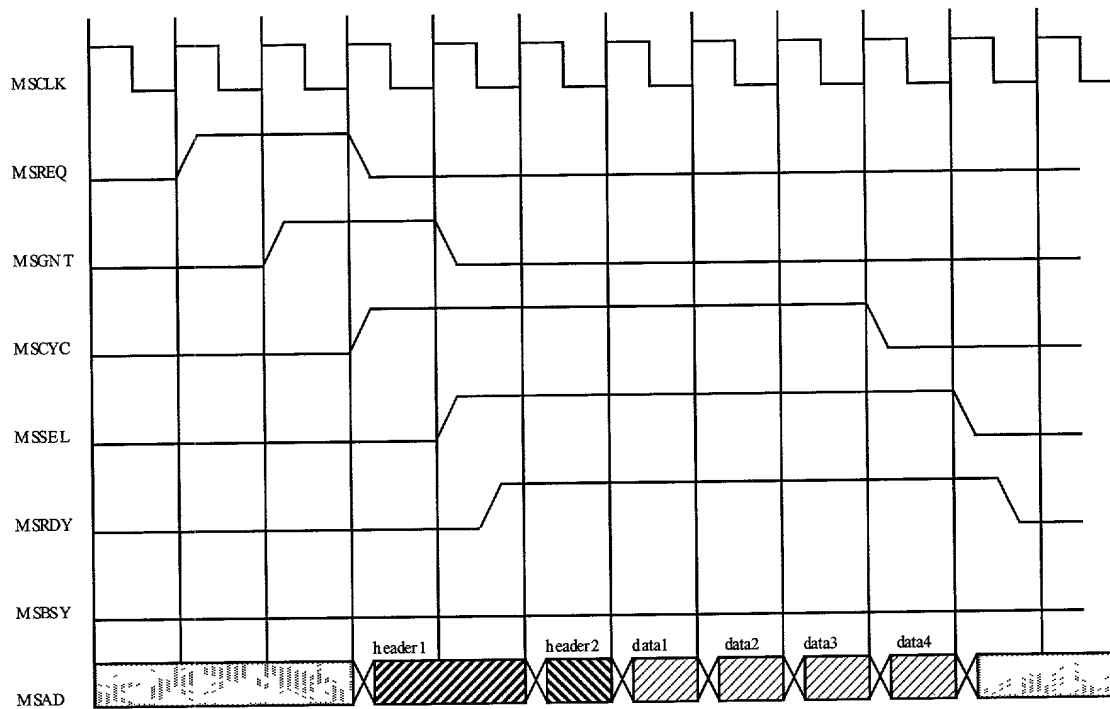
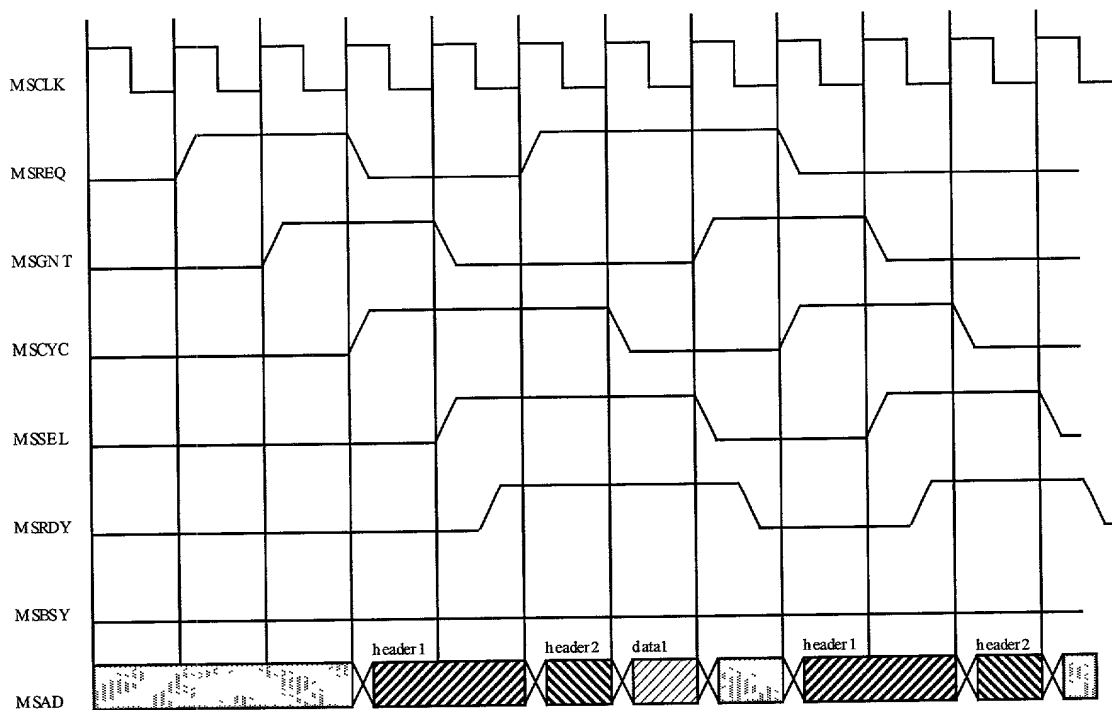
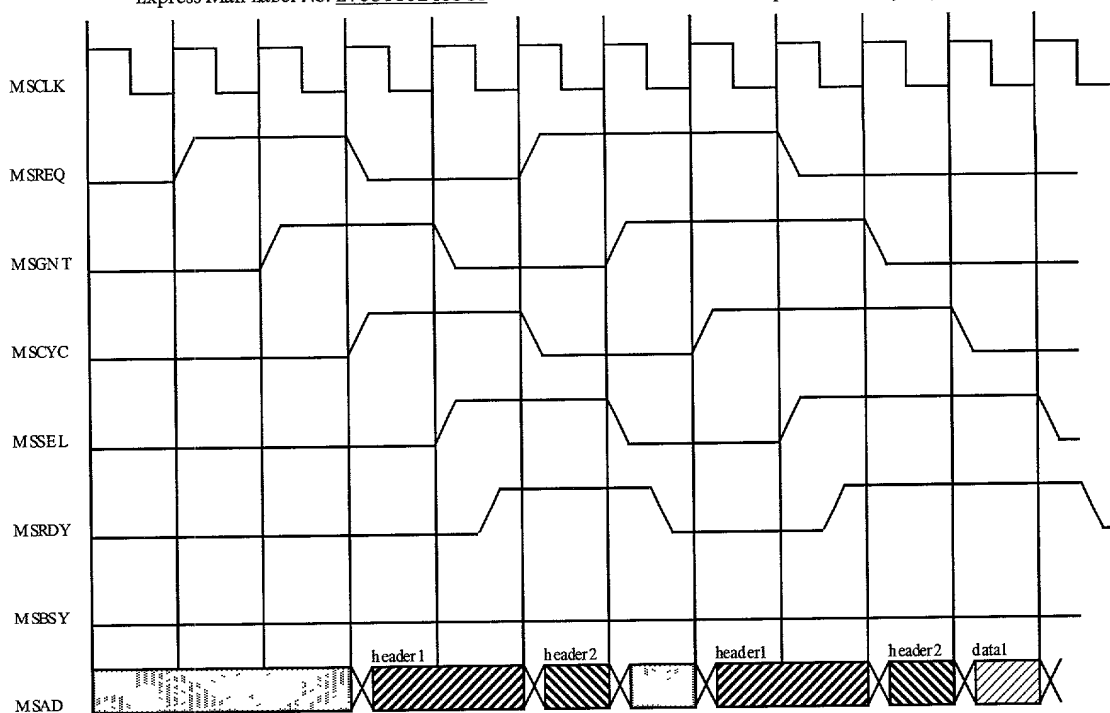


Figure 5b

**Burst Write Timing****Figure 6a**

**Write Followed by Read Timing****Figure 6b**



Read Followed by Write Timing (different devices)

Figure 6c

10035338 022307
20020228

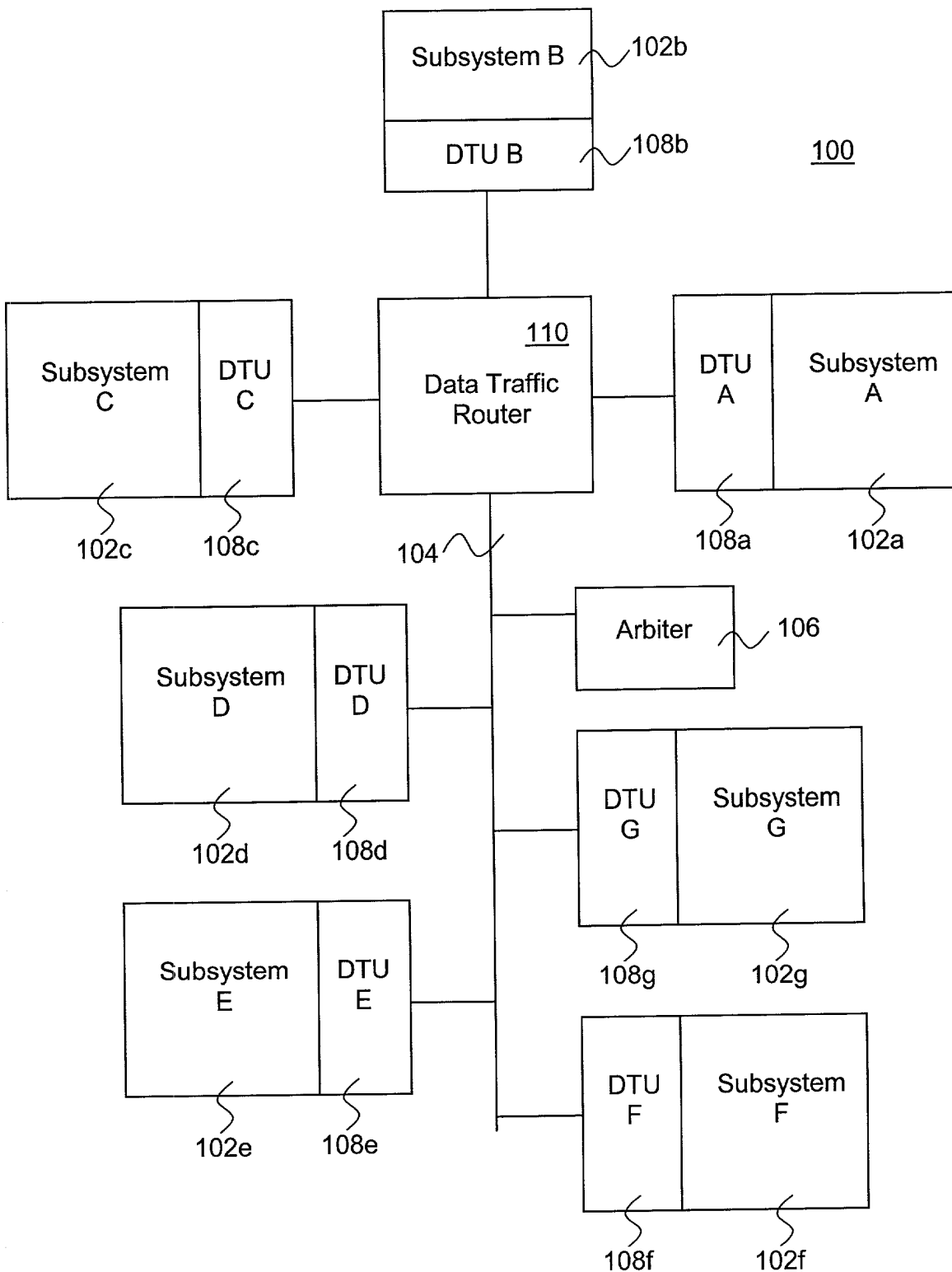


Figure 7

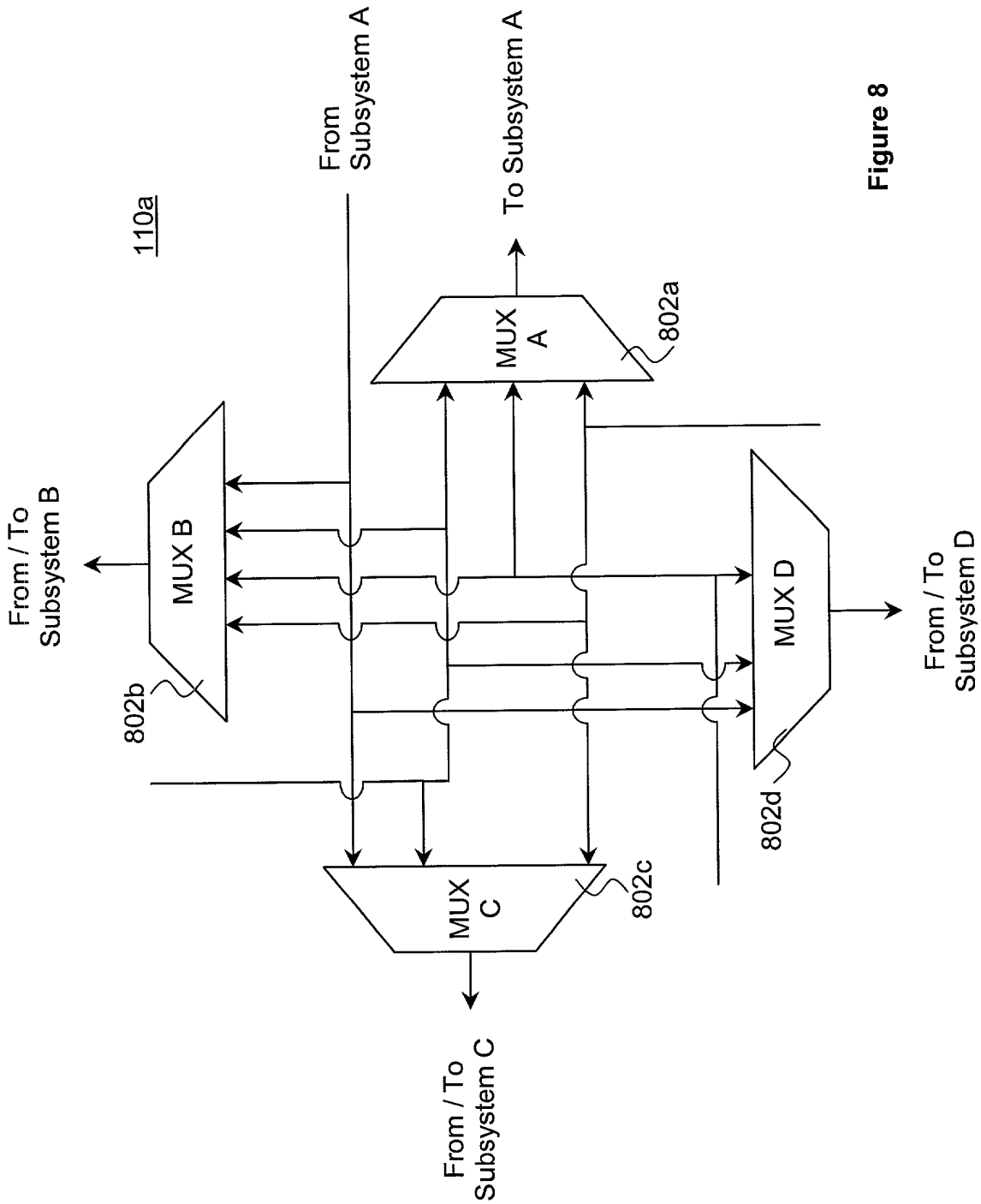


Figure 8

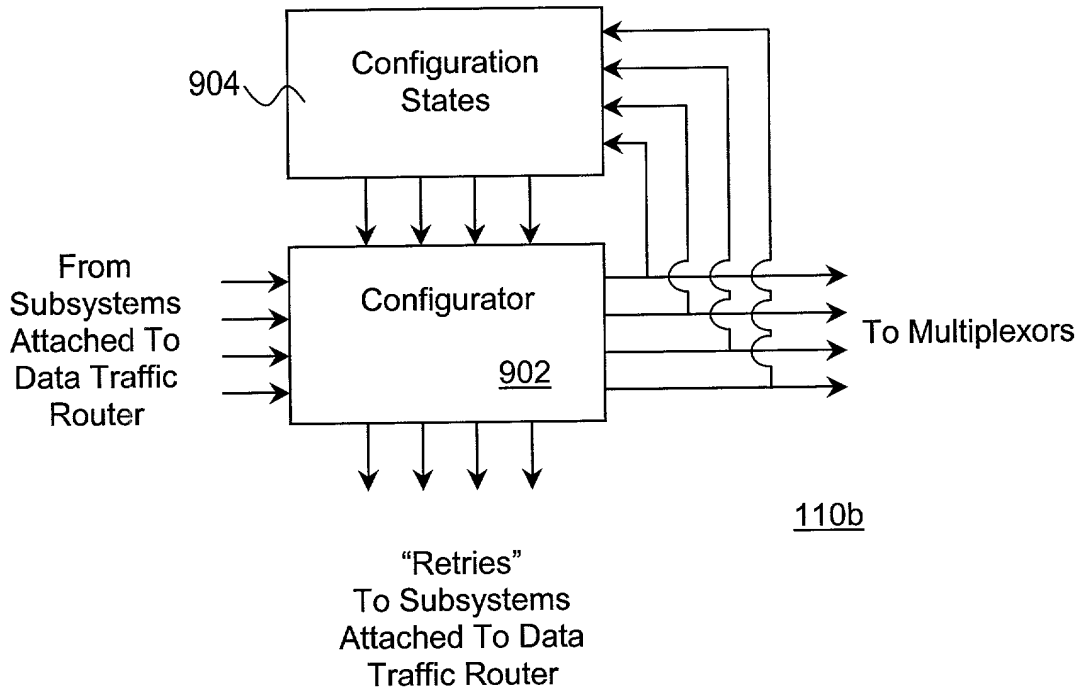


Figure 9

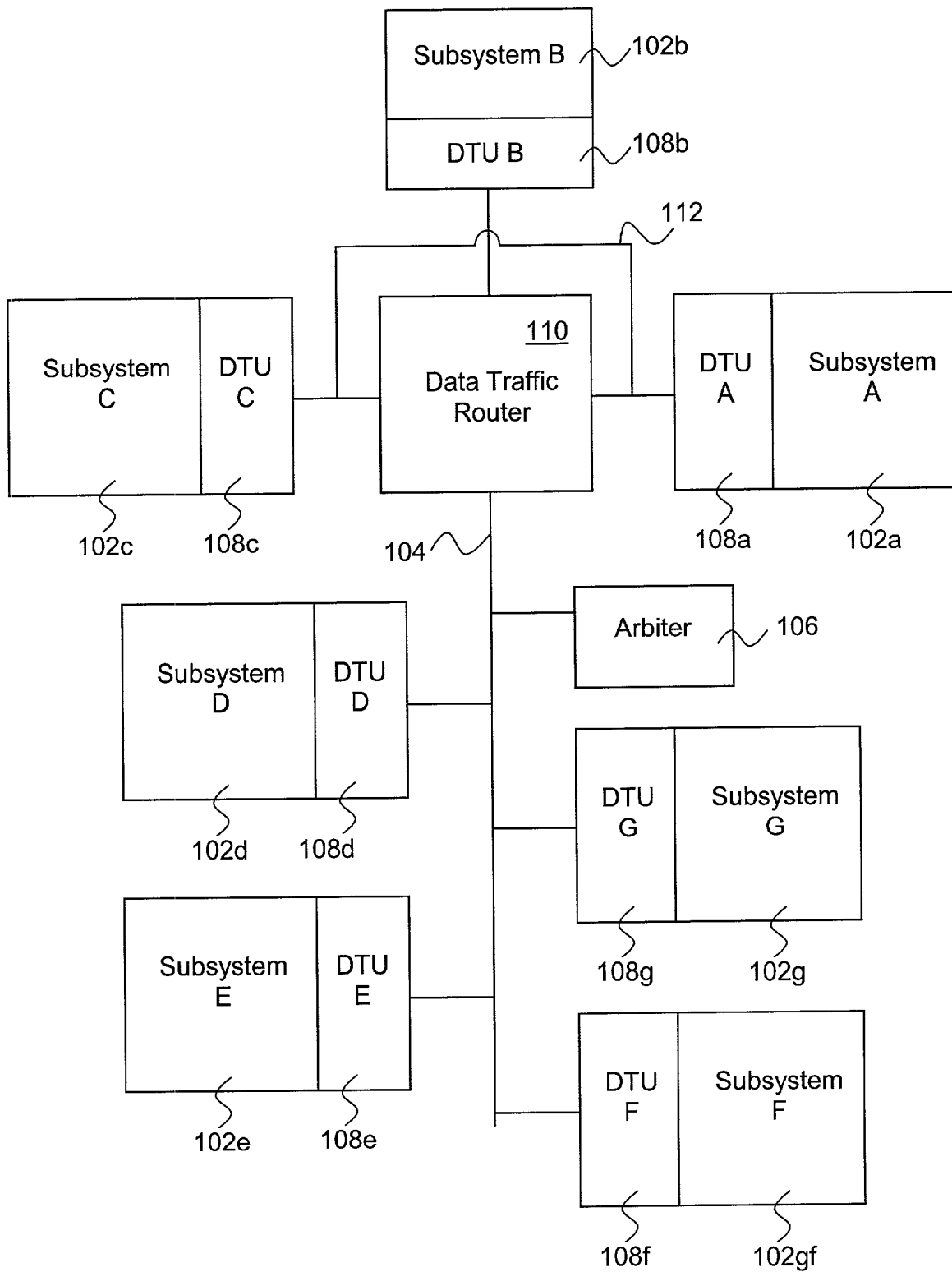


Figure 10

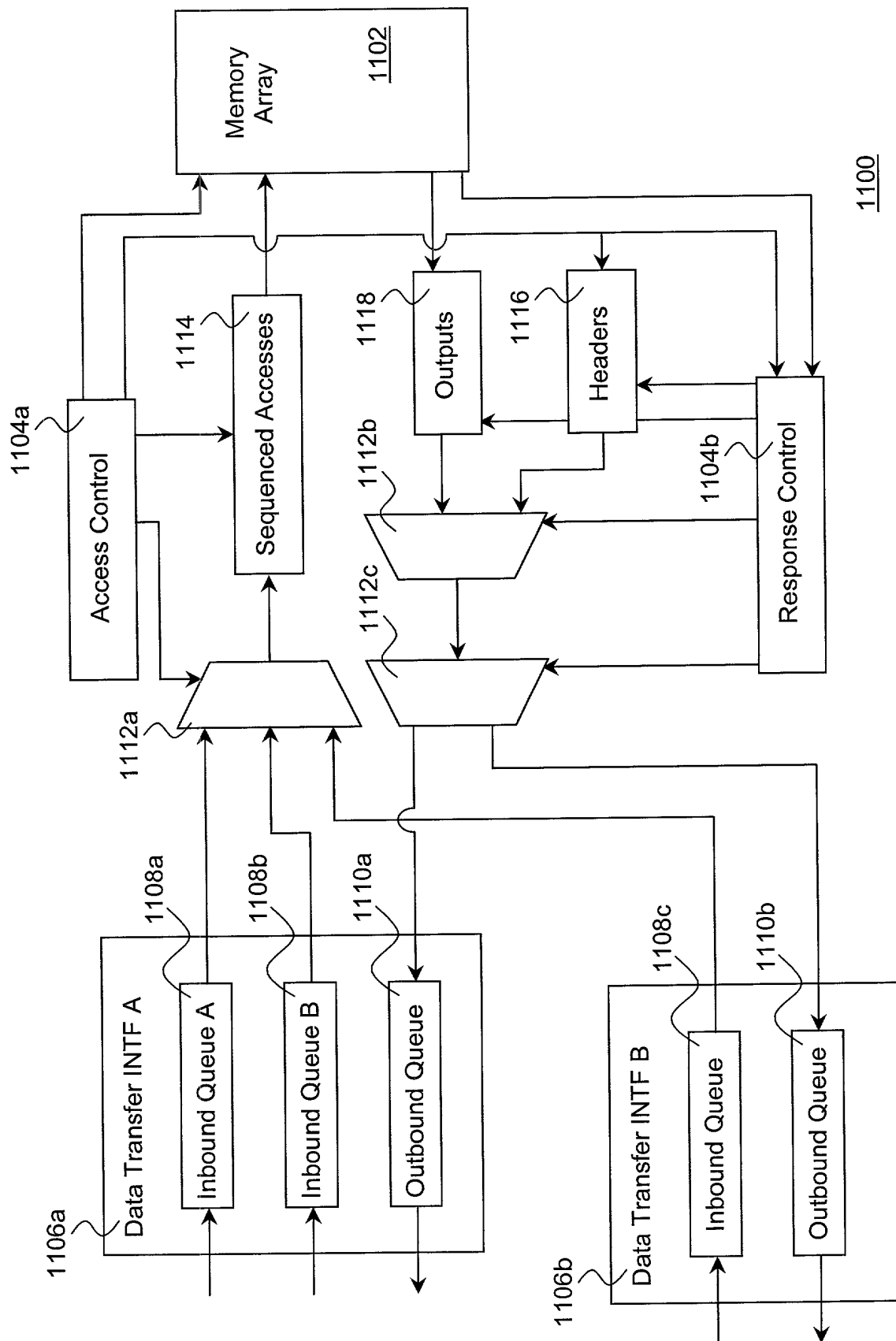


Figure 11